

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)**

<b>Serial Number</b>	10/801503
<b>Confirmation Number</b>	1822
<b>Filing Date</b>	March 15, 2004
<b>Title of Application</b>	A FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
<b>First Named Inventor</b>	Guido Gabriele ALBASINI
<b>Assignee</b>	STMicroelectronics, S.r.l.
<b>Group Art Unit</b>	2611
<b>Examiner</b>	Khanh C. Tran
<b>Attorney Docket Number</b>	2110-111-03
<b>Nature of the Office Communication to which this is responding</b>	Notice of Allowance
<b>Date of the Office Communication</b>	January 20, 2011
<b>Nature of this Document</b>	Comments on Statement of Reasons for Allowance

**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 15<sup>th</sup> day of April, 2011.

/Cindy McKee/

Signature

COMMISSIONER FOR PATENTS:

In response to the Notice of Allowability in this case mailed January 20, 2011, the Applicants, acting through their attorney, comment as follows. The Applicants thank the Examiner for allowing the claims. But the Applicants do not necessarily agree with the Examiner's Statement of Reasons for Allowance, and respectfully submit that the claims may be allowable for other reasons.

In the event additional fees are due as a result of this document, payment for those fees is enclosed. Should further payment be required to cover such fees you are hereby authorized to charge such payment to Deposit Account No. 07-1897.

If the Examiner believes that a phone interview would be helpful, he is respectfully requested to contact the Applicants' attorney, Kevin D. Jablonski, at (425) 455-5575.

Respectfully Submitted,

Graybeal, Jackson, LLP  
Representatives for Applicant

/Kevin D. Jablonski/ Dated: April 15, 2011  
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